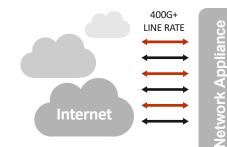
HASHCACHE

HASHCACHE FOR STATEFUL **NETWORK APPLIANCES**

Synogate HashCache manages context for network traffic at extreme speeds. It allows network appliances to process internet traffic packets in the context of the session they belong to, without being vulnerable to DDoS attacks. It can create new session context entries for more than 60 million connections per second and handle hundreds of millions of concurrent connections per memory channel on a single device.

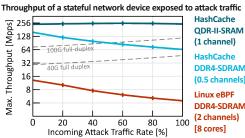
At its heart is an algorithm using Ordered Hash Tables, Linear Probing, and our own replacement strategy called Not Recently Sequenced (NRS) to use memory bandwidth extremely efficiently. It sports sub microsecond latencies and also guarantees that existing sessions will stay in memory for a certain amount of time. Synogate HashCache can be implemented as a digital circuit design on FPGAs or ASICs, and uses conventional DDR4-SDRAM.

Synogate HashCache is the ideal cache management solution for network devices like stateful firewalls, Intrusion Detection Systems, and large-scale TCP/UDP/QUIC servers, that are exposed to large volumes of attack traffic.



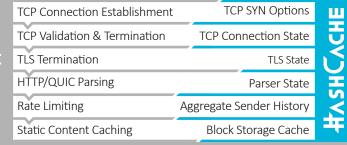
THROUGHPUT SCALES LINEARLY

The number of processed packets scales linearly with the number of memory channels due to high parallelism.



APPLICATIONS

- Stateful firewalls
- ► IDS
- ▶ NAT / CG-NAT
- DDoS mitigation
- Web & database caching
- High frequency trading
- ▶ Hardware TCP/UDP/QUIC server



GUARANTEED STATE RETENTION TIME

States have a minimum retention time which scales with the available storage and ensures that even under attack traffic, states of legitimate connections will not be dropped.

While conventional stateful packet processing solutions struggle to reach 40G, Synogate HashCache can easily handle any line rate even with DDR4-SDRAM by scaling up the number of memory channels.

In the c on the left, throughput is measured with real world traffic and attack traffic. Real world traffic is sourced from an internet exchange, while attack traffic consists of minimal packets that each create a new entry in the state memory.

FEATURES

- Very high insertion rate
- High read throughput
- ▶ Guarantees on state retention time
- Low latency (0.2 µs on external SRAM)
- ▶ SRAM or DRAM
- Low storage overhead
- ▶ Energy consumption < 1 µJ/request

STATEFUL **NETWORK**

PROCESSING AT LINE RATE

KEY BENEFITS

- Stateful processing in exposed networks
- Very resilient to DDoS attacks
- Designed for insert heavy use cases
- Supports billions of states
- Enables single device solutions
- Significant electricity savings
- Reduced TCO

Synogate is located in Berlin and specializes in RTL-design and FPGA development. With extensive knowledge in the fields of IP-networks, image processing, computer vision, deep learning, crypto, RISC-V ISA, data structures, algorithms, and math, we build custom solutions for our customers as well as generic IP-cores for licensing.

We are the authors of Gatery, a free open source framework for RTLdesign that increases productivity and allows us to build highly flexible and reusable components for our customers.

In 2022, we were awarded the StartUpSecure grant of the German BMBF. A patent application for Synogate HashCache is pending.





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of only about 4 bytes per entry, HashCache achieves a higher state density than comparable solutions.

OVERHEAD With a storage overhead



Service

DDR4-SDRAM (0.5 channels) DDR4-SDRAM

Due to a combined lookup, update, in-

sert and delete operation, on average

HashCache needs only 3 memory ac-

cesses for worst case traffic and as low

as 2.0003 for normal traffic.

COMBINED LUI

OPERATIONS